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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/995,161	11/26/2001	Sidharth Dalmia	062004-1950	9349
24504	7590	06/16/2004	EXAMINER	
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP 100 GALLERIA PARKWAY, NW STE 1750 ATLANTA, GA 30339-5948			NGUYEN, TUYEN T	
		ART UNIT	PAPER NUMBER	2832

DATE MAILED: 06/16/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

(Jn)

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/995,161	DALMIA ET AL.	
	Examiner TUYEN T NGUYEN	Art Unit 2832	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 12 March 2004.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1,3-5,8-11 and 45-49 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1,3-5,8-11 and 45-49 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
 Paper No(s)/Mail Date 2/19/2003
- 4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date. \_\_\_\_\_
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: \_\_\_\_\_

**DETAILED ACTION*****Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 3-5, 8-11 and 45-49 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sunahara [US 6,153,290] in view of Inoue et al. [US 5,270,493]

Sunahara discloses a multiplayer substrate [figures 1 and 3] comprising:

- a plurality of substrate layers [2-8]
- at least one conductor layer [14] formed on the upper surface of one of the plurality of substrate layers; and
- an inductor [11] *fabricated* on the upper surface of the first conductor layer.

Sunahara discloses the instant claimed invention except for the substrate layers formed of organic material and the inductor forms of a microstrip loop inductor.

Inoue et al. discloses a multi-layered printed circuit board [figures 1-2] comprising:

- a plurality of resin/epoxy based substrate layers [6, 15];
- a plurality of conductor layers [1, 7, 12, 16] formed on the substrate layers; and
- an electronic component [4] fabricated on at least one conductor layer.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to use the resin/epoxy based substrate layers for the substrate of Sunahara, as suggested by Inoue et al., for the purpose of improving performance.

Art Unit: 2832

It would have been obvious to one having ordinary skill in the art at the time the invention was made to use microstrip loop for the inductor of Sunahara for the purpose of reducing size of the substrate.

Regarding claims 5-9, Inoue et al. discloses the printed circuit board formed of the plurality of resin/epoxy based substrate layers and a plurality of conductor layers.

Regarding claims 46-49, the specific microstrip loop configurations/designs/sizes would have been an obvious design consideration based on the intended application/environment use.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to TUYEN T NGUYEN whose telephone number is 571-272-1996. The examiner can normally be reached on M-F 8:30-6:30.

The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TTN *TTr*

*Tuyen Nguyen*